CLAIMS

1. A Compact Peripheral Component Interconnect (CPCI) computer system, comprising:

a CPCI chassis;

a circuit board located within said CPCI chassis;

a first central processing unit (CPU) card coupled with said circuit board, said first CPU card having a first operating system;

a second CPU card coupled with said circuit board, said second CPU card having a second operating system;

a management agent located within said first CPU card, said management agent providing a management service; and

a management interface coupled with said management agent and said first and second CPU cards;

wherein said management agent provides said management service to said first and second CPU cards; and

wherein said management agent can be ran with said first and second operating systems via said management interface.

2. The CPCI computer system of Claim 1, further comprising a set of device nodes implemented with said management agent, said set of device nodes representing said first and second CPU cards.

3. The CPCI computer system of Claim 2, wherein said management agent comprises a managed object tree having a plurality of managed objects associated with said set of device nodes.

4. The CPCI computer system of Claim 3, wherein said set of device nodes are organized by using a device node tree.

5. The CPCI computer system of Claim 4, wherein said management interface comprises a plurality of events and wherein said managed object tree is kept in synchronization with said device node tree via said plurality of events.

6. The CPCI computer system of Claim 1, wherein said management interface comprises a first operating system (OS) independent application program interface (API) for allowing said management agent to establish a connection with said second CPU card.

7. The CPCI computer system of Claim 6, wherein said management interface comprises a second OS independent API for allowing said management agent to discover resources on said second CPU card.

8. The CPCI computer system of Claim 7, wherein said management interface comprises a third OS independent API for allowing said management agent to query for information from said second CPU card.

9. The CPCI computer system of Claim 1, further comprising a network interface coupled with said management agent for allowing said management agent to interface with said management interface.

10. The CPCI computer system of Claim 9, wherein said network interface comprises a Java Native Interface.

11. The CPCI computer system of Claim 1, wherein said management interface comprises a Common Operating System (Common OS) Application Program Interface (API).

12. A Compact Peripheral Component Interconnect (CPCI) computer system, comprising:

a CPCI chassis;

a circuit board located within said CPCI chassis;

a first central processing unit (CPU) card coupled with said circuit board, said first CPU card having a first operating system;

a second CPU card coupled with said circuit board, said second CPU card having a second operating system;

a management agent located within said first CPU card, said management agent providing a management service;

a management interface coupled with said first and second CPU cards; and

a network interface coupled with said management agent for allowing said management agent to interface with said first and second operating systems via said management interface;

wherein said management agent provides said management service to said first and second CPU cards.

13. The CPCI computer system of Claim 9, wherein said management agent can be ran with said first and second operating systems via said management interface and wherein said management agent needs to be ran with said first and second operating systems to provide said management service.

14. The CPCI computer system of Claim 9, wherein said management agent comprises a management model for providing a management protocol, a physical model for representing physical entities being managed, and an information model for providing platform specific information for said physical model.

15. The CPCI computer system of Claim 14, wherein said management model comprises one of a Simple Management Protocol, a Remove Method Invocation protocol, and a proprietary protocol.

16. The CPCI computer system of Claim 9, wherein said management interface comprises a Common Operating System (Common OS) Application Program Interface (API).

17. The CPCI computer system of Claim 16, wherein said network interface comprises a Java Native Interface.

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18. A Compact Peripheral Component Interconnect (CPCI) computer system, comprising:

a CPCI chassis;

a circuit board located within said CPCI chassis;

a first central processing unit (CPU) card coupled with said circuit board, said first CPU card having a first operating system;

a second CPU card coupled with said circuit board, said second CPU card having a second operating system;

a first management agent located within said first CPU card, said first management agent providing a first local management service for said first CPU card and a global management service for said first and second CPU cards;

a second management agent located within said second CPU card, said second management agent providing a second local management service for said second CPU card;

a management interface coupled with said first and second CPU cards; and

a network interface coupled with said management agent for allowing said first and second management agents to interface with said first and second operating systems via said management interface;

wherein said first management agent provides said global management service with said first and second operating systems via said management interface.

19. The CPCI computer system of Claim 18, wherein said second management agent provides a management information about said second CPU card to said first management agent via said management interface.

20. The CPCI computer system of Claim 19, wherein said first management agent uses said management information about said second CPU to provide said global management service for said first and second CPU cards.